

# **K Band Packaged Low Noise Amplifier**



### Key Features

- 21-27 GHz Bandwidth
- 21 dB Nominal Gain
- 2.5 dB Nominal Noise Figure
- 10 dBm Nominal P1dB
- Bias: 3.5 V, 60 mA
- Package Dimensions:

4.0 x 4.0 x 1.1 mm (0.157 x 0.157 x 0.043 in)

## Measured Data

#### Bias Conditions: Vd = 3.5 V, Id = 60 mA

Point-to-Point Radio

**Primary Applications** 

• Point-to-MultiPoint Radio

## **Product Description**

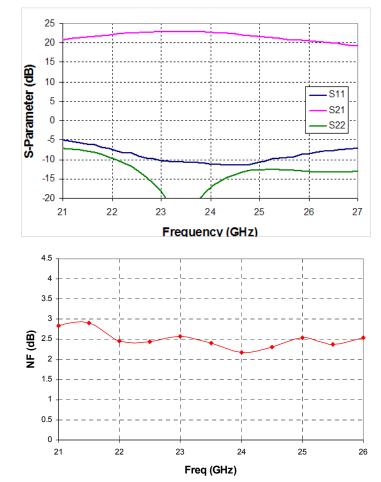
The TriQuint TGA4506-SM is a K-Band Packaged low noise amplifier. The TGA4506-SM operates from 21-27 GHz and is designed using TriQuint's power pHEMT production process.

The TGA4506-SM typically provides 2.5 dB noise figure and 21 dB small signal gain.

The TGA4506-SM is ideally suited for Point-to-Point Radio and Point-to-Multipoint Radio Communications.

Evaluation Boards are available upon request.

Lead-Free & RoHS compliant.



Datasheet subject to change without notice





 TABLE I

 MAXIMUM RATINGS <u>1</u>/

SYMBOL	PARAMETER	VALUE	NOTES
Vd	Drain Voltage	5 V	<u>2/</u>
Vg	Gate Voltage Range	-1.5 TO 0V	
ld	Drain Current	190 mA	<u>2</u> /
Ig	Gate Current	6 mA	
P <sub>IN</sub>	Input Continuous Wave Power	9 dBm	
P <sub>D</sub>	Power Dissipation	0.86 W	<u>2</u> / <u>3</u> /
Т <sub>сн</sub>	Operating Channel Temperature	200 °C	<u>4</u> /
	Mounting Temperature (30 Seconds)	260 °C	
T <sub>STG</sub>	Storage Temperature	-65 to 150 °C	

1/ These ratings represent the maximum operable values for this device.

2/ Combinations of supply voltage, supply current, input power, and output power shall not exceed P<sub>D</sub>.

- 3/ When operated at this bias condition with a package base plate temperature of 85 °C, the median life is 1.5E+3 hrs.
- 4/ Junction operating temperature will directly affect the device median time to failure (Tm). For maximum life, it is recommended that junction temperatures be maintained at the lowest possible levels.



# **TGA4506-SM**

#### TABLE II ELECTRICAL CHARACTERISTICS

(Ta = 25 °C Nominal)

PARAMETER	TYPICAL	UNITS
Drain Voltage, Vd	3.5	V
Drain Current, Id	60	mA
Gate Voltage, Vg	-0.5 to 0.0	V
Small Signal Gain, S21	21	dB
Input Return Loss, S11	11	dB
Output Return Loss, S22	15	dB
Noise Figure, NF	2.5	dB
Output Power @ 1 dB Compression Gain, P1dB	10	dBm



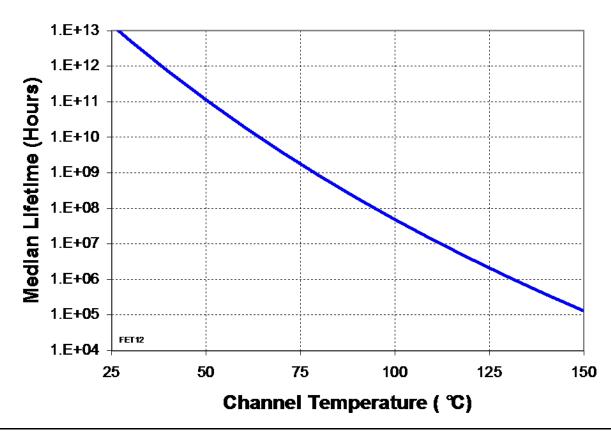


#### TABLE III THERMAL INFORMATION

Parameter	Test Conditions	Т <sub>сн</sub> (°С)	θ <sub>JC</sub> (°C/W)	Tm (HRS)
<ul> <li>θ<sub>JC</sub> Thermal</li> <li>Resistance</li> <li>(channel to backside</li> <li>of package)</li> </ul>	Vd = 3.5V I <sub>D</sub> = 0.06 A Pdiss = 0.21 W	113	133	1.5E+6

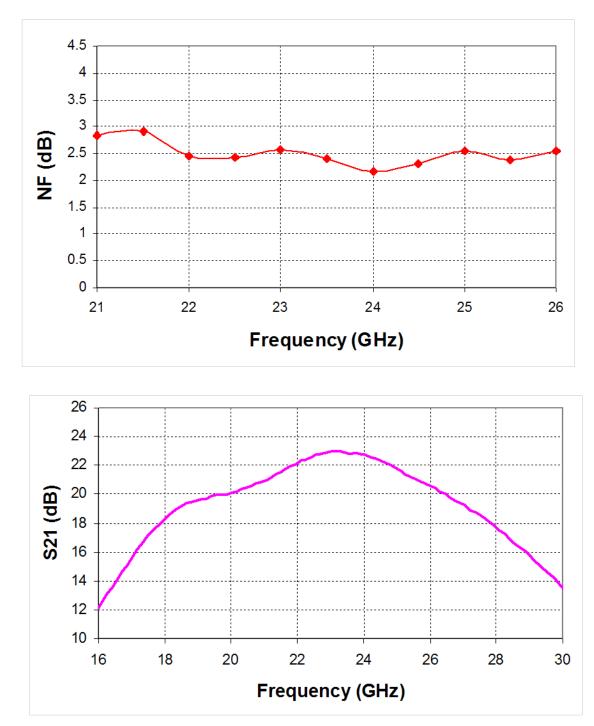
Note: Package backside on board at 85 °C.

# Median Lifetime (Tm) vs. Channel Temperature









## **Measured Data**

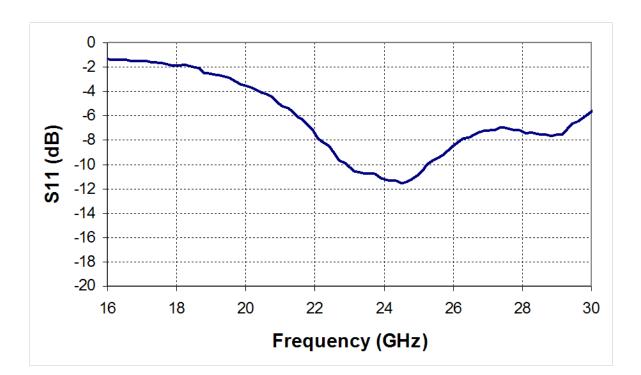
Bias Conditions: Vd = 3.5 V, Id =60 mA

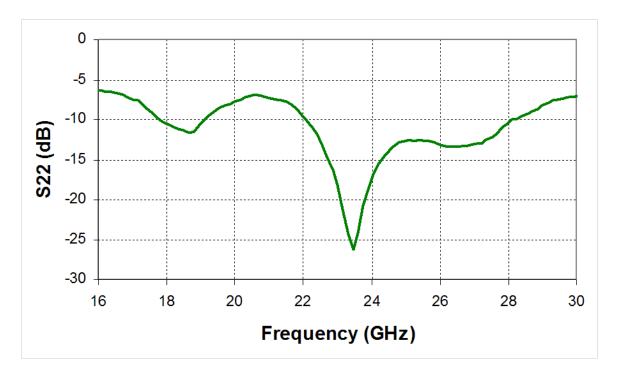




# **Measured Data**

Bias Conditions: Vd = 3.5 V, Id =60 mA



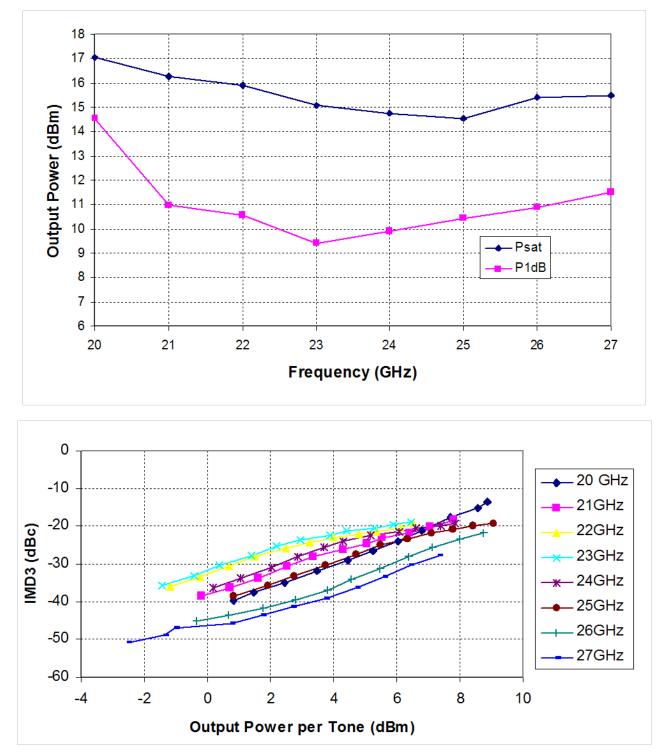






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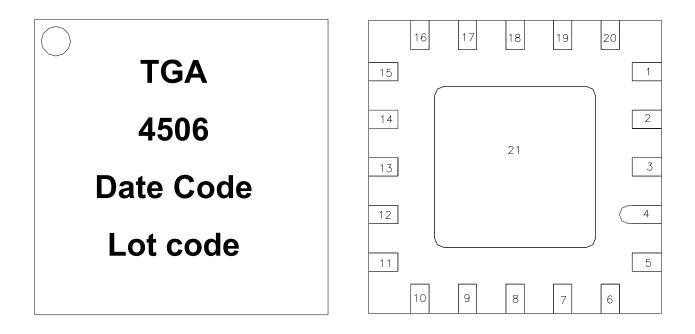
Bias Conditions: Vd = 3.5 V, Id =60 mA







# Package Pinout Diagram



**Bottom View** 

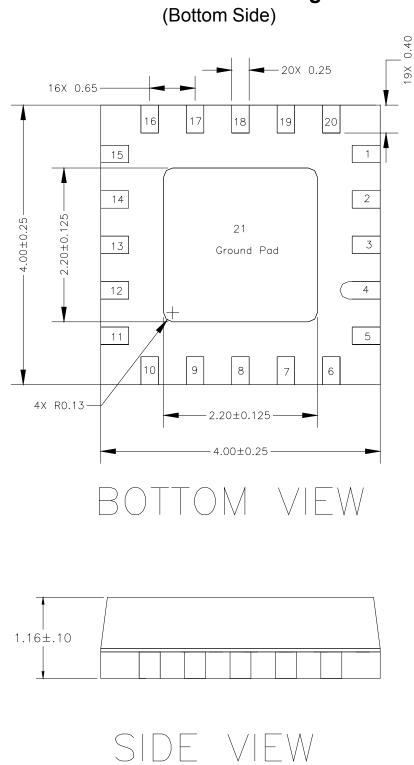
Top View

Dot indicates Pin 1

Pin	Description
1,5,6,10,11,15,16, 20,21	GND
2,4,8,9,12,14,17,18	N C
3	RF Input
1 9	V g 1
1 3	RF Output
7	V d 1





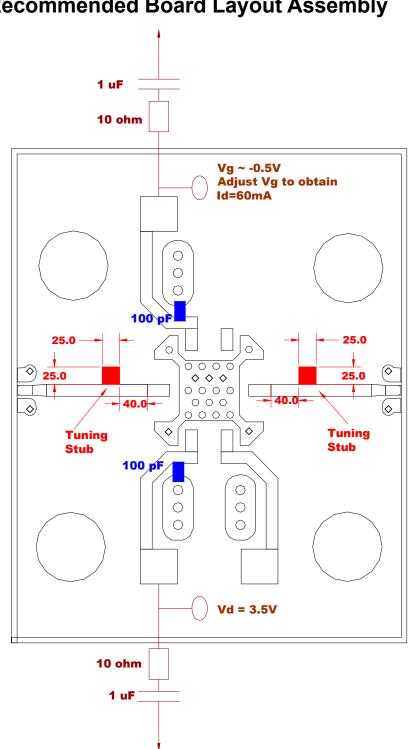


Mechanical Drawing (Bottom Side)

GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.







# **Recommended Board Layout Assembly**

#### All measurements were made with the part soldered to 0.008 in thick RO4003





#### **Recommended Surface Mount Package Assembly**

Proper ESD precautions must be followed while handling packages.

TriQuint recommends using a conductive solder paste for attachment. Follow solder paste and reflow oven vendors' recommendations when developing a solder reflow profile. Typical solder reflow profiles are listed in the table below.

Hand soldering is not recommended. Solder paste can be applied using a stencil printer or dot placement. The volume of solder paste depends on PCB and component layout and should be well controlled to ensure consistent mechanical and electrical performance.

Compatible with high volume surface mount assembly processes using no-clean flux

Reflow Profile	SnPb	Pb Free
Ramp-up Rate	3 °C/sec	3 °C/sec
Activation Time and Temperature	60 – 120 sec @ 140 – 160 °C	60 – 180 sec @ 150 – 200 °C
Time above Melting Point	60 – 150 sec	60 – 150 sec
Max Peak Temperature	240 °C	260 °C
Time within 5 °C of Peak Temperature	10 – 20 sec	10 – 20 sec
Ramp-down Rate	4 – 6 °C/sec	4 – 6 °C/sec

### **Typical Solder Reflow Profiles**

#### **Ordering Information**

Part	Package Style
TGA4506-SM	QFN 4x4 Surface Mount